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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/922,479	08/03/2001	Thomas Zettler	J&R-0694	7212
24131	7590	07/02/2004	EXAMINER	
LERNER AND GREENBERG, PA P O BOX 2480 HOLLYWOOD, FL 33022-2480			CHUNG, PHUNG M	
		ART UNIT	PAPER NUMBER	
		2133		
DATE MAILED: 07/02/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/922,479	ZETTLER	
	<b>Examiner</b>	<b>Art Unit</b>	
	Phung My Chung	2133	

**— The MAILING DATE of this communication appears on the cover sheet with the correspondence address —**

#### **Period for Reply**

**A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.**

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### **Status**

- 1) Responsive to communication(s) filed on \_\_\_\_\_.
- 2a) This action is **FINAL**.                          2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### **Disposition of Claims**

- 4) Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1-28 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### **Application Papers**

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### **Priority under 35 U.S.C. § 119**

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) All    b) Some \* c) None of:
    1. Certified copies of the priority documents have been received.
    2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### **Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____.   |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>6 and 7</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|  | 6) <input type="checkbox"/> Other: _____.                                   |

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1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-28 are rejected under 35 U.S.C. 102(b) as being anticipated by Krug (4,961,053).

As per claims 1-4, Krug discloses a circuit arrangement for testing integrated circuit components, which performing the functions of:

Providing an integrated circuit that includes a self-test device;

Starting to perform a test of the integrated circuit with the self-test; and

Subsequently, connecting the integrated circuit to an external testing device that performs a function selected from the group consisting of reading out results of the test and evaluating the results of the test. (See col. 3, line 25 to col. 4, line 21 and col. 6, lines 19-41).

As per claims 5-6, Krug further discloses with the self-test device, writing data into a test result memory; and

After testing the integrated circuit with the self-test device, taking out of operation, components of the integrated circuit that are not needed to continue to store the data in the test result memory. (See col. 8, lines 49-53).

As per claims 7-8, Krug further discloses wherein taking at least part of the integrated circuit out of operation includes not supplying a clock signal or a supply voltage to the integrated circuit. (See col. 6, lines 52-55).

As per claims 9-10, Krug further discloses providing the plurality of integrated circuits on at least one wafer. (See abstract, col. 1, lines 9-11, col. 2, lines 55-66).

As per claim 11-15, these claims are also rejected under the same rationale as set forth in claims 1-4.

As per claim 16, Krug discloses a circuit arrangement for testing integrated circuit components, comprising:

A components;

A self-test device for testing the components; and

A device for, at a particular time, taking specific ones of the components out of operation, wherein the particular selected from the group consisting of during the testing and after the testing. (See col. 6, lines 19-55).

As per claims 17-23, these claims are also rejected under the same rationale as set forth in claims 6-8.

As per claims 24-28, these claims are also rejected under the same rationale as set forth in claims 1-23.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phung My Chung whose telephone number is 703-305-9686. The examiner can normally be reached on Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 703-305-9595. The fax phone numbers

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for the organization where this application or proceeding is assigned are (703) 872-9306  
for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or  
proceeding should be directed to the receptionist whose telephone number is 703-305-  
3900.



PHUNG W. CHUNG  
PRIMARY EXAMINER